

EE/CpE 423-424  
WEEKLY STATUS REPORT

<b>Group #</b>	<b>10</b>	<b>Week ending: (Saturday)</b>	<b>10/27/07</b>	<b>Report #</b>	<b>7</b>
<b>Project Title:</b>	Pandora's Box				
<b>Group Leader:</b>	Jason Travis	<b>Advisor:</b>	Prof. Tewskbury		
<b>Sponsor/Client:</b>					
<b>Total number of person-hours spent on project by group during past week:</b>				<b>16</b>	
<b>Is project on schedule?</b>		<b>Yes</b>	<input checked="" type="checkbox"/> <b>[X]</b>	<b>No</b>	<input type="checkbox"/> <b>[ ]</b>

<b>Weekly status:</b>
<p>When we met this week as a group, we started working on the details of the project that are needed for the final proposal. Joe and Mike are going to work on the hardware aspect of the project. They will create a block diagram for the board, they will choose an appropriate FPGA for our needs and they will create a basic schematic. This work will be done over the next few weeks and will be worked into the final proposal. Derek and Jason will be working on the software side of the project. They will be working on the basic algorithm for encryption, a basic software flow chart and a simulation to test the algorithms. As with the hardware, this work will be done over the next few weeks. They will attempt to get a simulation going as soon as possible to ensure that the algorithm will not be too complex for the FPGA that is selected.</p>

**Weekly report is due to Senior Design Coordinator and TA by Noon Monday. Email submission is greatly preferred - submit using WebCT, email to sd@koala.ece.stevens-tech.edu or individual emails. Copy should be sent to advisor**