

EE/CpE 423-424  
WEEKLY STATUS REPORT

<b>Group #</b>	<b>10</b>	<b>Week ending: (Saturday)</b>	<b>10/13/07</b>	<b>Report #</b>	<b>5</b>
<b>Project Title:</b>	Pandora's Box				
<b>Group Leader:</b>	Jason Travis	<b>Advisor:</b>	Prof. Tewksbury		
<b>Sponsor/Client:</b>					
<b>Total number of person-hours spent on project by group during past week:</b>				<b>24</b>	
<b>Is project on schedule?</b>		<b>Yes</b>	<input checked="" type="checkbox"/> <b>[X]</b>	<b>No</b>	<input type="checkbox"/> <b>[ ]</b>

<p><b>Weekly status:</b></p> <p>During the past week we had two meetings about our project. We had a meeting with Professor Haibo He to discuss the feasibility of using VHDL to perform our encryption/decryption. We discussed what type of FPGA we would need and how we should go about designing this algorithm in VHDL. We found that it is possible to do in VHDL and we will need a chip with about 50K gates to perform the needed functions. We also met with Professor Tewksbury this week. At this meeting we discussed more in depth what this device will look like. We will need a serial-to-parallel convertor, some encryption/decryption logic in the middle, and a parallel-to-serial convertor on the other end. Professor Tewksbury suggested we spend some time designing a sufficient algorithm that is not too complex yet performs the needed security. We started researching some methods of doing this and will start to narrow them down and decide on one. Also, we met this week to compile our midterm project proposal, taking into account the conversations we had with both Professor He and Tewksbury.</p>
---

**Weekly report is due to Senior Design Coordinator and TA by Noon Monday. Email submission is greatly preferred - submit using WebCT, email to sd@koala.ece.stevens-tech.edu or individual emails. Copy should be sent to advisor**